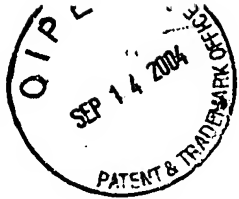


Figure 1



Title: APPARATUS AND METHOD FOR SAMPLE-AND-HOLD  
WITH BOOSTED HOLDING SWITCH  
Inventors: Maria Rosaria Tursi and Robert Callaghan Taft  
Docket No.: 08211/1200663-US1/P05913

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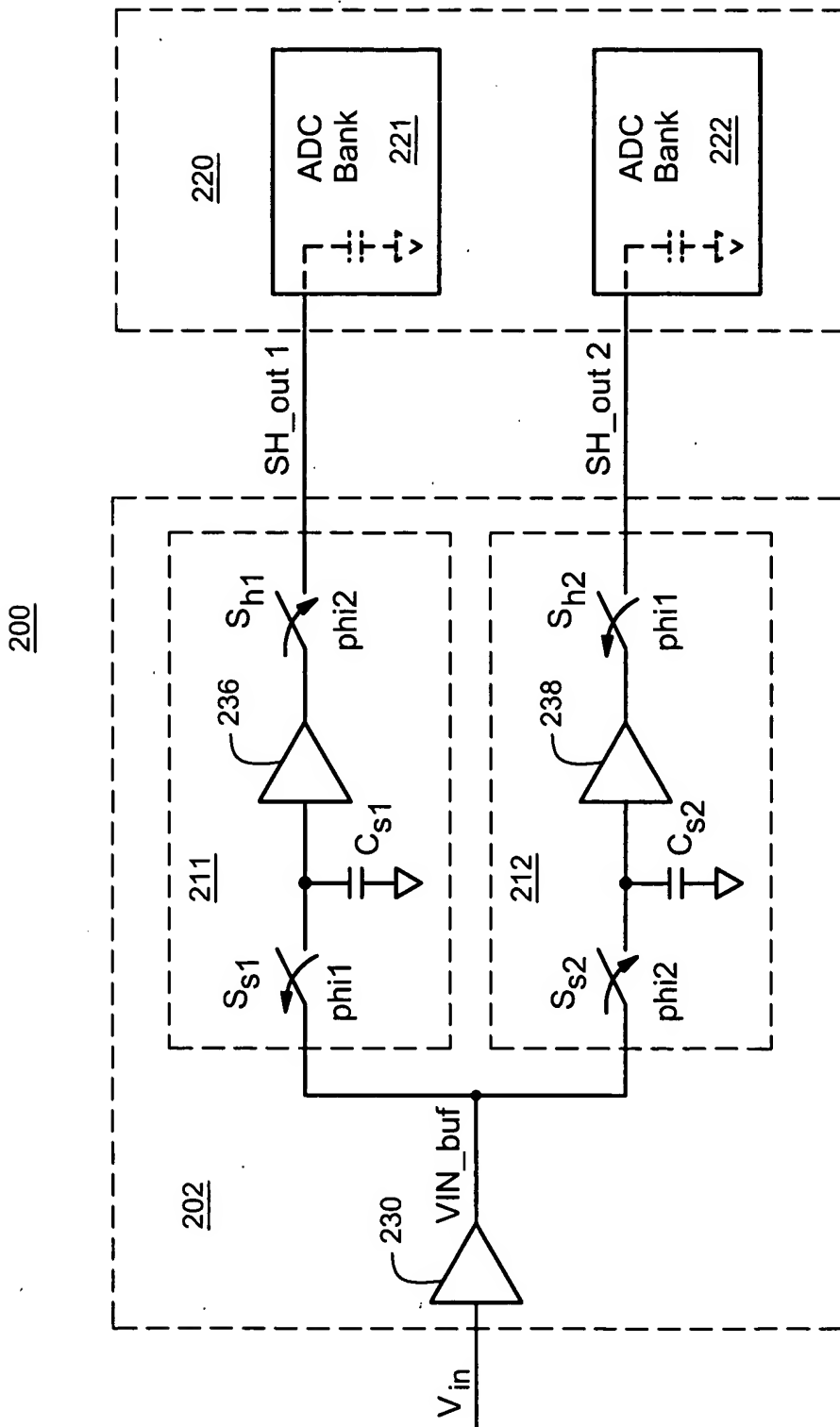
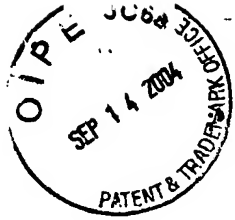


Figure 2



Title: APPARATUS AND METHOD FOR SAMPLE-AND-HOLD  
WITH BOOSTED HOLDING SWITCH  
Inventors: Maria Rosaria Tursi and Robert Callaghan Taft  
Docket No.: 08211/1200663-US1/P05913

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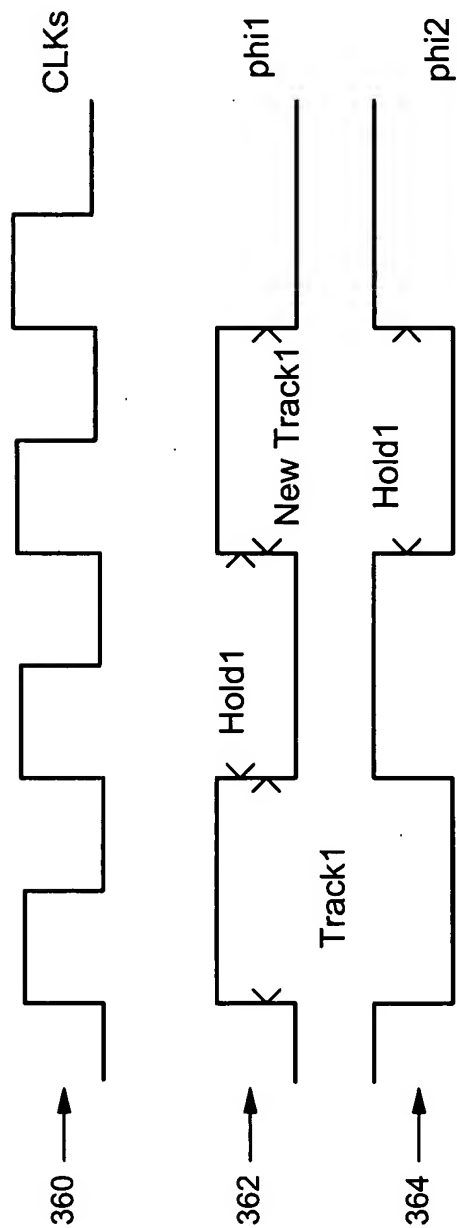


Figure 3

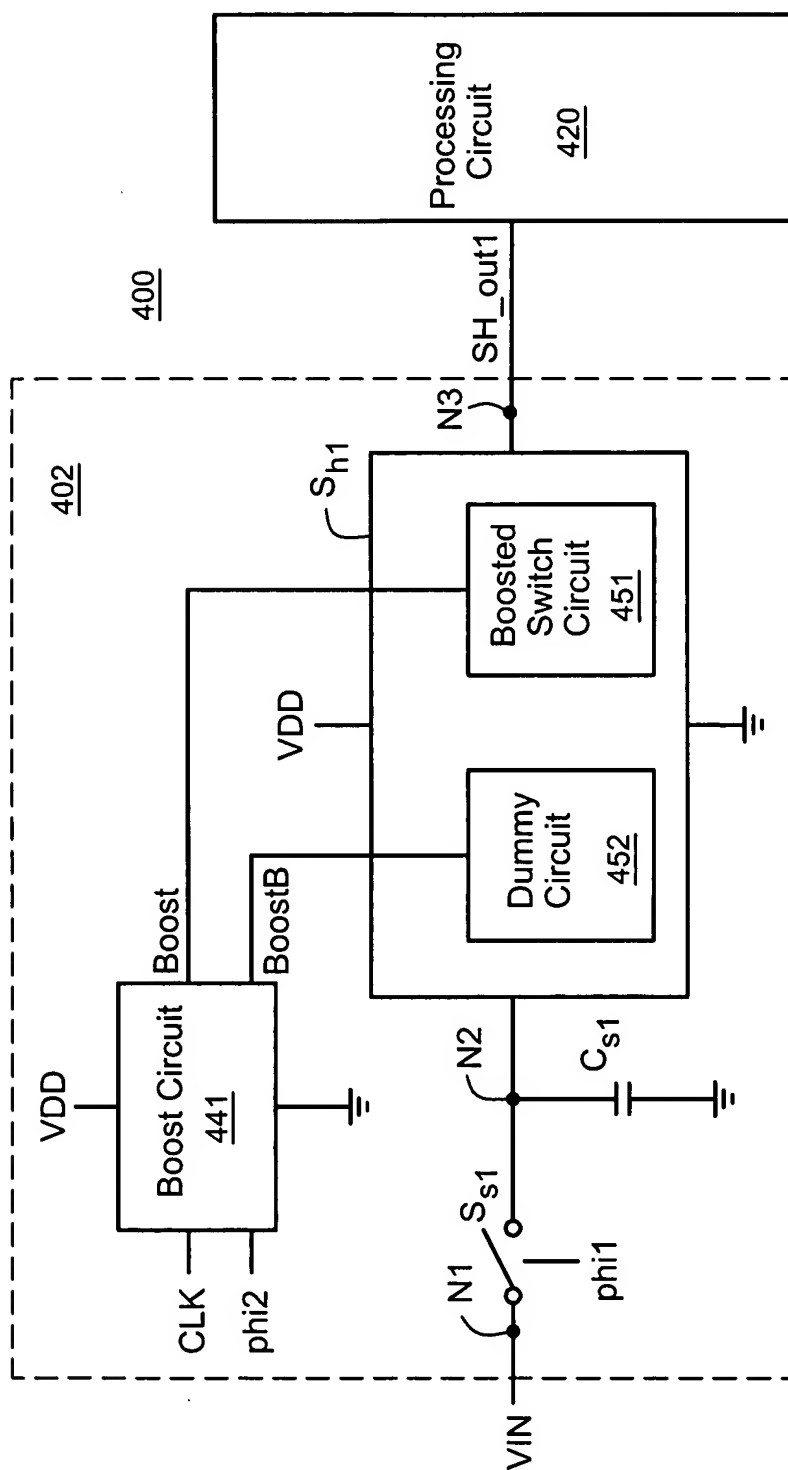


Figure 4

**Title: APPARATUS AND METHOD FOR SAMPLE-AND-HOLD  
WITH BOOSTED HOLDING SWITCH**  
**Inventors: Maria Rosaria Tursi and Robert Callaghan Taft**  
**Docket No.: 08211/1200663-US1/P05913**

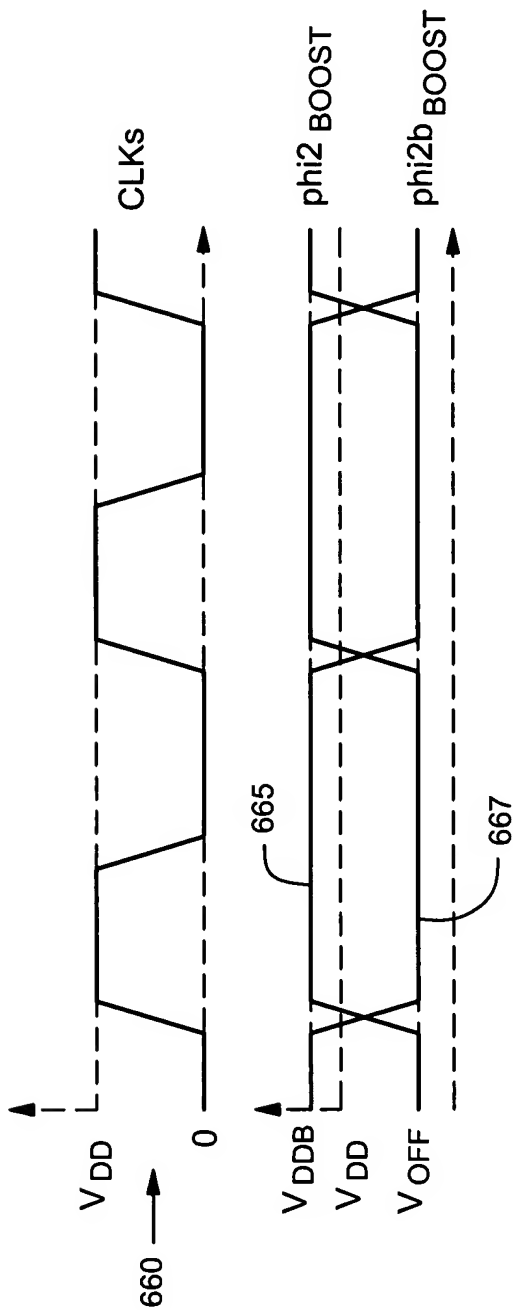
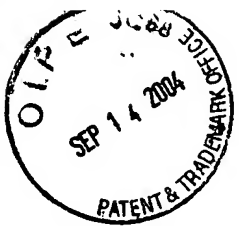


Figure 6





Title: APPARATUS AND METHOD FOR SAMPLE-AND-HOLD  
WITH BOOSTED HOLDING SWITCH  
Inventors: Maria Rosaria Tursi and Robert Callaghan Taft  
Docket No.: 08211/1200663-US1/P05913

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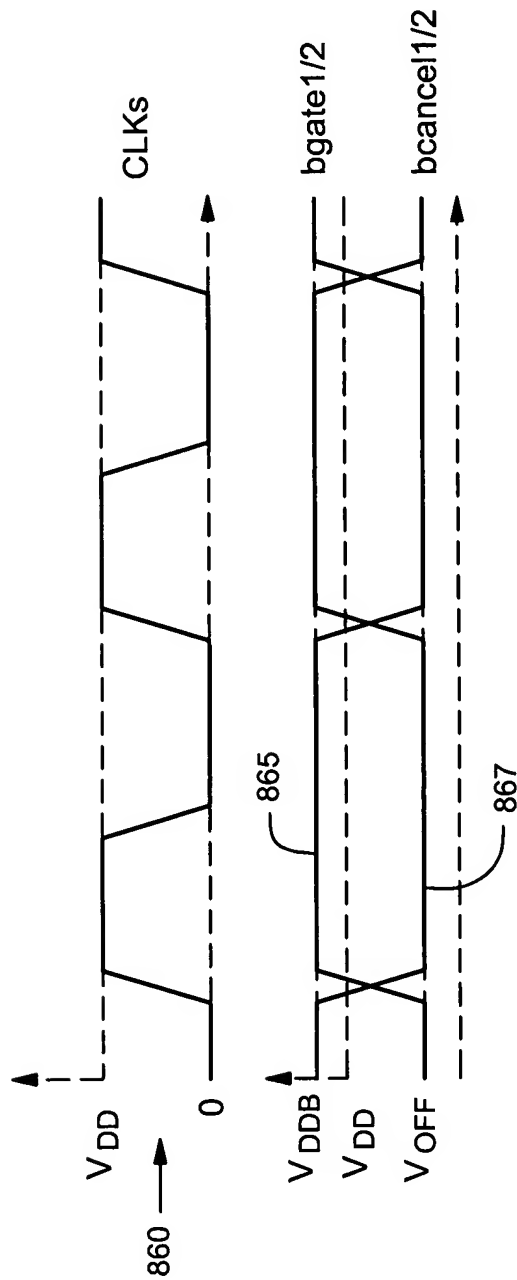


Figure 8



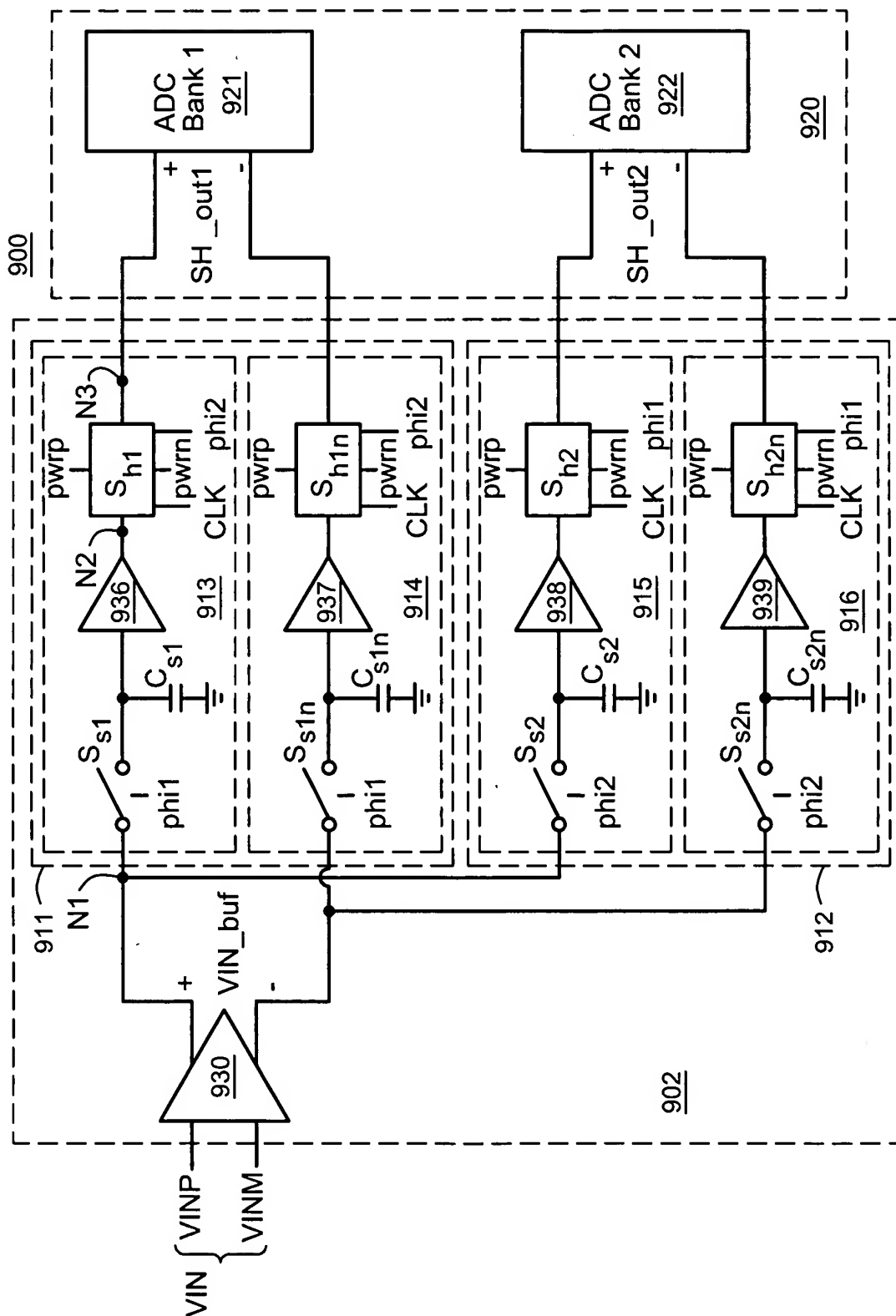


Figure 9

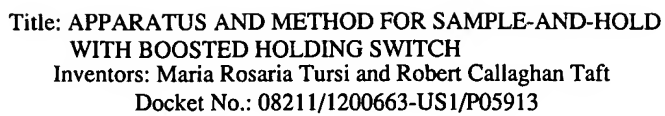
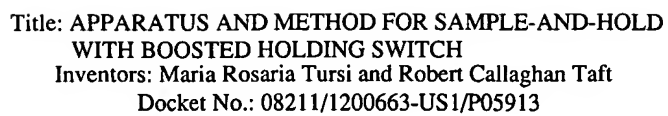


Figure 10 is a schematic diagram of a circuit 1070. The circuit includes a boost circuit 1040 with inputs CLK and pwrp, and outputs posbst and pwrn. The posbst signal is connected to a cross-coupled pair of transistors M8 and M9, which are part of a larger structure 1090. The pwrn signal is connected to a network of transistors M10, M11, M12, and M13. The circuit also includes capacitors C3 and C4, and nodes labeled bgate1, bcancel1, phi2, trackd, and trackb. The output of the circuit is pwrn.

# Figure 10

[illegible]

# Figure 11